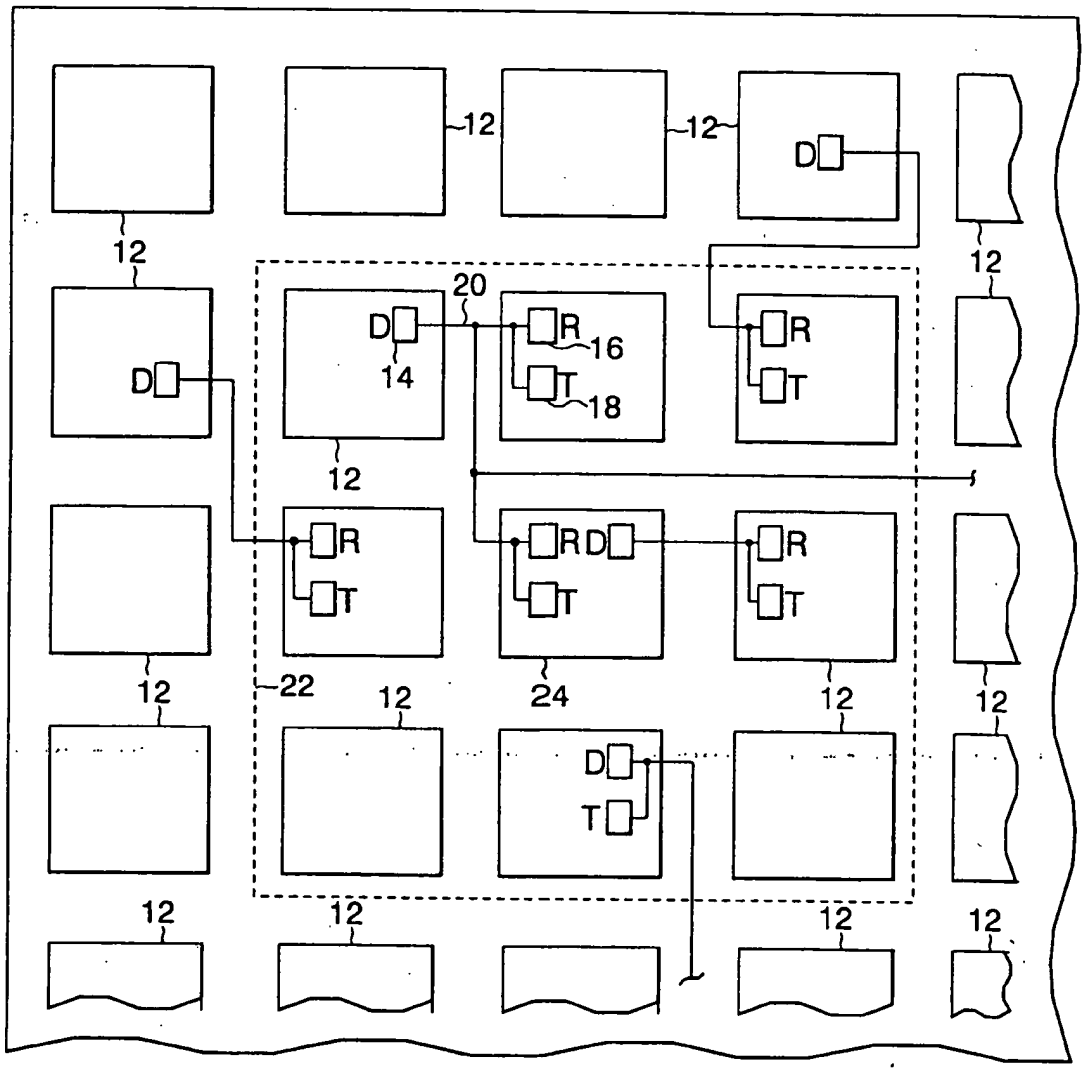
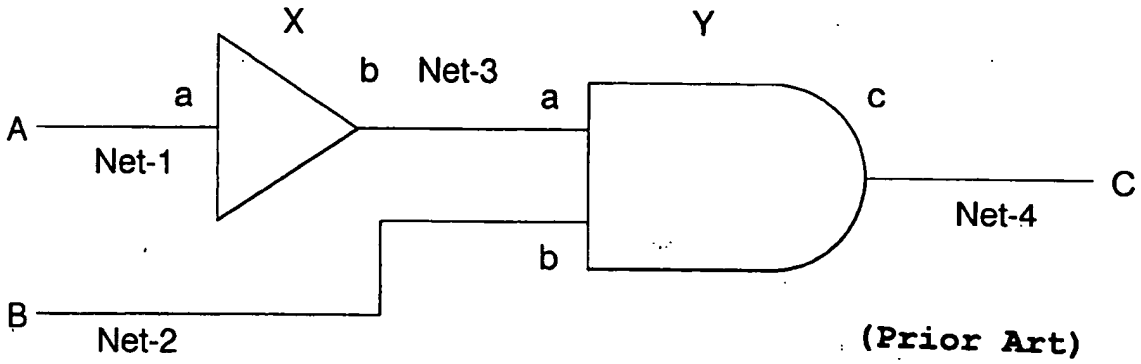
10



(Prior Art)

Figure 1

REVIEWED
OK
2-26-05



(Prior Art)

EXAMPLE OF LOGIC CIRCUIT

Figure 2

```
Port:
  INPUT : A, B:
  OUTPUT: C
EndPort
Net:
  Net-1: pA, Xpa;
  Net-2: pB, Ypb;
  Net-3: Xpb, Ypa;
  Net-4; pC, Ypc;
EndNet;
```

(Prior Art)

EXAMPLE OF NET LIST

Figure 3

Reviewed "OK"

1-6-04

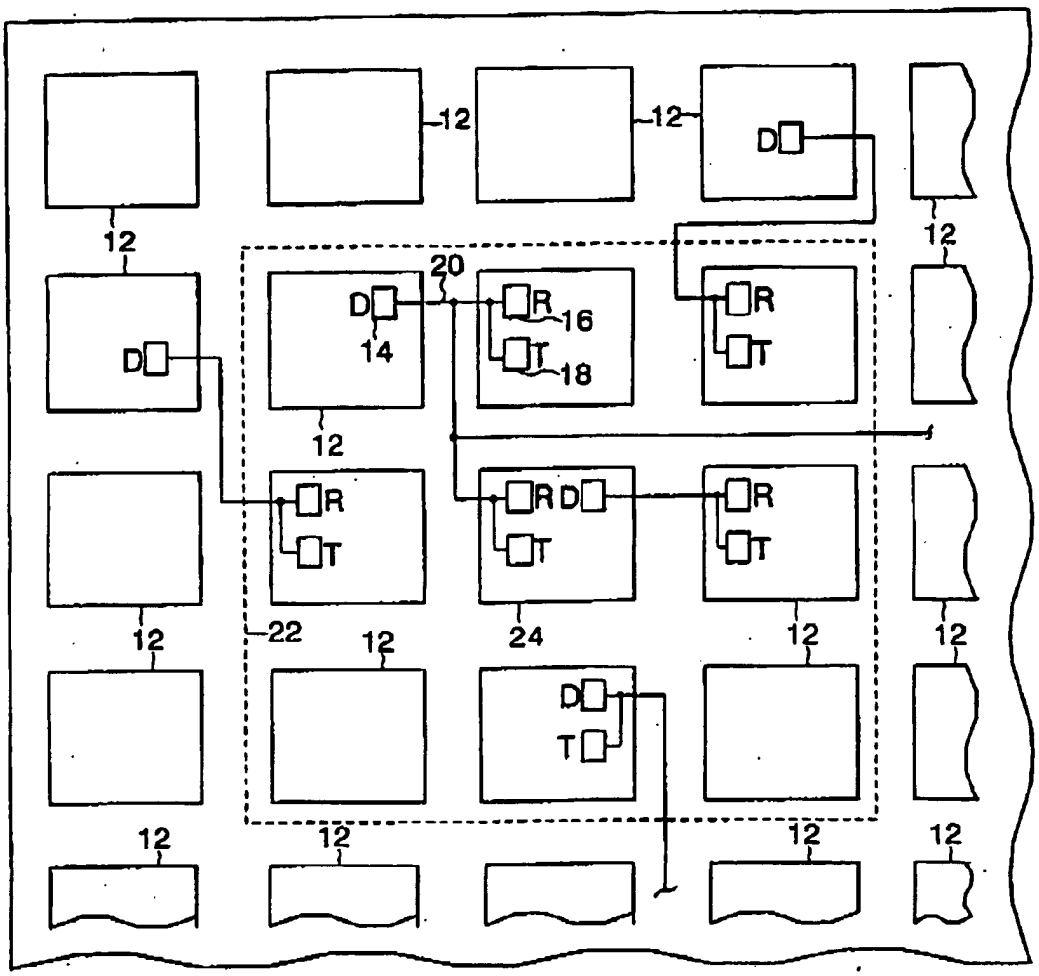
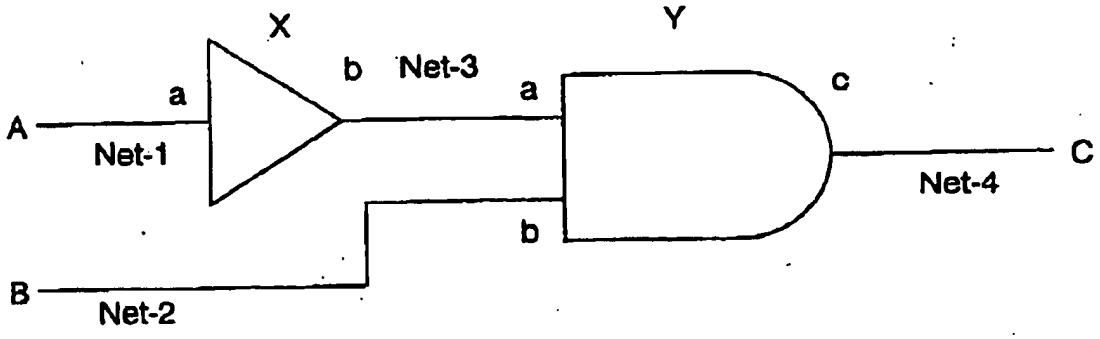


Figure 1 (Prior Art)

Reviewed "OK"
JSR
1-6-05



EXAMPLE OF LOGIC CIRCUIT

Figure 2 (Prior Art)

```
Port:
  INPUT : A, B:
  OUTPUT: C
EndPort
Net:
  Net-1: pA, Xpa;
  Net-2: pB, Ypb;
  Net-3: Xpb, Ypa;
  Net-4; pC, Ypc;
EndNet;
```

EXAMPLE OF NET LIST

Figure 3 (Prior Art)